

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L9	0	(714/726.ccls.) and ((integrated adj circuit) with normal with test \$3 with mode with scan\$4) and (reset\$4 with flip-flops with (transition adj time) with between with (test adj mode) with (normal adj mode) with (scan adj mode adj signal))	US-PGPUB; USPAT; USOCR	OR	OFF	2008/05/24 19:42
L10	0	(714/727.ccls.) and ((integrated adj circuit) with normal with test \$3 with mode with scan\$4) and (reset\$4 with flip-flops with (transition adj time) with between with (test adj mode) with (normal adj mode) with (scan adj mode adj signal))	US-PGPUB; USPAT; USOCR	OR	OFF	2008/05/24 19:45

L11	0	(714/730.ccls.) and ((integrated adj circuit) with normal with test \$3 with mode with scan\$4) and (reset\$4 with flip-flops with (transition adj time) with between with (test adj mode) with (normal adj mode) with (scan adj mode adj signal))	US-PGPUB; USPAT; USOCR	OR	OFF	2008/05/24 19:45
L12	0	(714/733.ccls.) and ((integrated adj circuit) with normal with test \$3 with mode with scan\$4) and (reset\$4 with flip-flops with (transition adj time) with between with (test adj mode) with (normal adj mode) with (scan adj mode adj signal))	US-PGPUB; USPAT; USOCR	OR	OFF	2008/05/24 19:45
L13	0	(714/724.ccls.) and ((integrated adj circuit) with normal with test \$3 with mode with scan\$4) and (reset\$4 with flip-flops with (transition adj time) with between with (test adj mode) with (normal adj mode) with (scan adj mode adj signal))	US-PGPUB; USPAT; USOCR	OR	OFF	2008/05/24 19:46

L14	0	(714/42.ccls.) and ((integrated adj circuit) with normal with test \$3 with mode with scan\$4) and (reset\$4 with flip-flops with (transition adj time) with between with (test adj mode) with (normal adj mode) with (scan adj mode adj signal))	US-PGPUB; USPAT; USOCR	OR	OFF	2008/05/24 19:46
L15	0	(714/25.ccls.) and ((integrated adj circuit) with normal with test \$3 with mode with scan\$4) and (reset\$4 with flip-flops with (transition adj time) with between with (test adj mode) with (normal adj mode) with (scan adj mode adj signal))	US-PGPUB; USPAT; USOCR	OR	OFF	2008/05/24 19:46
L16	0	(365/154.ccls.) and ((integrated adj circuit) with normal with test \$3 with mode with scan\$4) and (reset\$4 with flip-flops with (transition adj time) with between with (test adj mode) with (normal adj mode) with (scan adj mode adj signal))	US-PGPUB; USPAT; USOCR	OR	OFF	2008/05/24 19:47

L17	0	((integrated adj circuit) with normal with test \$3 with mode with scan\$4) and (reset\$4 with flip-flops with (transition adj time) with between with (test adj mode) with (normal adj mode) with (scan adj mode adj signal))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/24 19:47
L20	1	kayukawa-yoshitaka.in.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/05/24 19:55
L21	34	aoki-tetsuya.in.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/05/24 19:56
L23	1	hamaguchi-takahiro.IN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/05/24 19:58
L24	1	oshima-noriyuki.IN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/05/24 19:58

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